

Multilevel resistive switching memory based on two-dimensional materials using simple solution process

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Abstract Resistive switching memory has attracted great attention due to easy fabrication with simple structure as well as outstanding non-volatile memory performance including fast switching speed and low power consumption.¹⁻³ As a method of maximizing information storage density, the multi-level cell application (MLC) is one of the most promising solutions. The MLC in resistive switching memory has been widely reported in various materials such as polymers and binary metal oxides.⁴⁻⁶ However, an MLC operation based on only two dimensional materials has not been demonstrated yet. Here, we report the multilevel resistive switching memory based on graphene oxide (GO) and MoS₂ using simple solution process. MoS₂ nanosheets, which is exfoliated by Li intercalation process, were successfully embedded between two GO thin films using a spin-coating process. The GO/MoS₂/GO stacks result in significant On/Off current ratio increases from $\sim 10^2$ for GO-only devices to $> 10^4$ for devices with MoS₂ nanosheet. Excellent multilevel non-volatile memory performance including $>10^4$ s retention time, $>10^2$ endurance cycle without severe degradation, and at least four multiple resistance states were also demonstrated. Furthermore, we systematically investigated the resistive switching mechanism that trapped space charges in MoS₂ deep trap sites determine the number of resistance states which could be modulated by electrical bias. These results indicate that MoS₂ nanosheets could be utilized as good charge storage materials enabling the MLC operation.

References

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